



ITS Technical Coordination Report

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ITS plenary – 18/06/2024

1 CERN 2 University of Liverpool

Interventions

- May 6th
 - <u>L6_00</u>: stave undervoltage on April 30th \rightarrow CAEN power supply board (A3009B) replacement
 - L5 15: RU CAEN trips on April 12th and April 29th → replaced the power patch cords and swapped the RU power channels
 - L3 22, L3 23, L6 12, L6 16 and L6 23: significant PU/trimmer offsets → Power Board (PB) patch cord replacements
- May 8th
 - <u>L6_16</u>: RU trip \rightarrow RU power cable replacement
- June 10th (during TS1)
 - <u>Reflashing RU scrubbing images</u> in TS1
- June 13th (during TS1)
 - <u>L5_15/L5_14</u>: no trip found since May 8th, reverted the swapped RU power cables
 - <u>L4_03, L5_07, L6_10, L6_31 and L1_01</u>: PB patch cord replacements
 - L4_03, L5_07, L6_10, L6_31: significant PU/trimmer offsets
 - L1_01: two HIC/PU trips in the previous few weeks





DCS

- CRU monitoring now directly uses the Kafka library
- Significant performance improvements achieved through modified configuration and code
- Error-increase variables in link monitoring allowing differentiate transient and persistent issues
- No more EOR checks based on CRU data (replaced by real-time alarms on persistent issues)
- Auto-recovery
 - Issue where recovery was not running on all lanes for a stave fixed
- Persistent Timebase loss of lock errors during clock transition fix in place, to be checked with next clock transistions
- Prepare-For-Run (PFR)
 - Always force reconfiguration between runs (simplifies FSM, in practise reconfiguration was always used)
- ITSComm database interface
 - Stuck in LOADING when changing detector configuration ongoing
- WinCC/FSM
 - CRU link counters available in RU counter panel
 - Improved handling of PFR in FSM to avoid blocking in CONFIGURING
 - Bug fixes related to loading of mixed DB versions after ITSComm being stuck
 - Enhanced transition states: MONITORING_OFF state color changed to yellow

CRU Link counter

	Status	Packets processe	Packet protocol E	Corrupt SO	Corrupt EOI	Corrupt RDI	Length mismat	CRC mismat	Unexpected index	Packet link ERR	Packet link ERR D/	Oversize	FEC	Glitch	SOR orbi Error	Incr PHY e	rro Ag	ge De	ala
0	1	0	0) 0	0	0	0	0	0	C) () 0		335 16925668	0	0	0	1	3
1	1	0	0	0	0	0	0	0	0	0) () 0	1	115 50070929	0	0	0	1	3
2	1	0	0) 0	0	0	0	0	0	0) () 0		803 17431167	0	0	0	0	2



Thanks to the FLP and central DCS teams for their ongoing support!

CRU/RU



- Discarding of packets with corrupted RDH is now available in CRU (thanks to the central CRU team)
- RU PU controller power interlock [ongoing]
 - No power interlock triggered when a SEU induced bit flipping of a back-bias channel occurs
 - Solution: turn off the channels chained to the same BIAS channel and set the power interlock
 - Pull request merged; a new RU firmware version to be released

To be monitored



- L1_01: HIC/PU tripped twice in the previous weeks \rightarrow will be further monitored
- L4_01: RU tripped last week. This is the first occurrence, and the current trend is stable → will be further monitored
- Cooling
 - No new visible issues developed since last intervention in February
- PU and trimmer voltage offsets
- SEU protection in RU FW
 - Data corruption currently our main EoR reason
 - To be better understood with the help of the new CRU monitoring
 - Upset rate for Pb-Pb operation at 50 kHz to be estimated once pp operation is consolidated
- Threshold and fake-hit rate (refer to Jory's report for the detailed detector (IB) recalibration)

ITS2 ToDo list



- A dedicated **ToDo list** being prepared and maintained by (D)TC
 - Issue/task updates will be discussed in the weekly ITS2 TC meeting
 - Includes summary table, detailed tables for each topic, and links to detailed notes for each issue/task

1	Торіс	Summary	Assigned to	Priority Status		Last update Remarks											
2	Operation	Threshold scan: Not-OK lanes in several (~5) IB staves	Artem, Jory, Ivan	High	•	In pro 🔻	0	3/6/2024 TS1, tests; retuning detector fil	rst								
3	Operation	SOR and EOR can't be propagated to daughter nodes if the half-stave/layer is in MIXED state	Sergey, Artem, Jory	High	•	In pro 🔻	0	3/6/2024 TS1									
4	Operation	Several staves stuck in NOT_SAFE when moving to SAFE	Sergey, Artem, Jory	High	•	In pro 🔻	0	3/6/2024 TS1									
5	Operation	Voltage stability during PHYS runs	Artem, Jory, Markus, Felix	High	•	(In pro 🔻	0	3/6/2024									
6	Operation	IB chips constantly in ERROR	Artem, Jory	High	•	(In pro 🔻	1	7/6/2024 TS1, DTU settings									
7	Operation	Decoding errors related to auto-recovery	Artem, Nicolo', Markus	Medium		In pro 🔻	1	7/6/2024									
8	ITSComm	CRU Monitoring with Kafka (including performance problems)	Markus, Sergey	Urgent		(In pro 🔻	no > 10/6/2024 TS1, tests										
9	ITSComm	Hard-RU in-run recovery (XKCU re-program)	Markus, Sergey, Felix	High	•	(In pro 🔻											
10	ITSComm	Soft-RU in-run recovery (reset everything but power control)	Markus, Sergey, Felix	High	•	In pro	A	В	с	D	E	F	G	H I	J	к	
11	ITSComm	ITSComm database interface	Sergey, Markus	High	•	In pro 1 Op	ration										
12	ITSComm	EOR checks	Markus	Medium		In pro ² ID		Summary	Assigned to	Priority	Date of entry	Status	Description	Last update Remarks	JIRA (if applicable)		
13	ITSComm	Additional data points to be added to ADAPOS	Markus	Medium			1	Threshold scan: Not-OK lanes in several (~5) IB sta	Artem, Jory, Ivan	High 🔻	31/5/2024	In pro 🔻	Link to Doc	03/6/2024 TS1, tests; retuning	etector first => dedug the pr	oblematic st	
14	ITSComm	Short dry run before SOR	Markus, Sergev	Medium		Open 5	4	SOR and EOR can't be propagated to daughter not Several stayes stuck in NOT_SAFE when moving to	Sergey, Artem, Jory Sergey Artem Jory	High V	31/5/2024	In pro V	Link to Doc	03/6/2024 TS1			
15	ITSComm	Automatic pixel mask refresh	Markus	Medium	-		8	Voltage stability during PHYS runs	Artem, Jory, Markus, Felix	High 💌	31/5/2024	In pro 🔻	Link to Doc	03/6/2024			
16	ITSComm	Flashing RU EW images	Markus Felix	Low	-	Open ⁷	2	IB chips constantly in ERROR	Artem, Jory	High 🔹	03/6/2024	In pro 🔻	Link to Doc	17/6/2024 TS1, DTU settings			
17	ITSComm	Use CRU DMA status in calibration scan instead of 60s wait	Markus	Low	•	Open a	3	Stave stuck in CONFIGURING when processing PF	Sergey, Artem, Jory	High 🔻	31/5/2024	In pro V	📙 📑 ITS2_	Issue_Details	0 C	×	
18	WinCC	Panels lost during LII undate and WN splitting	Sergey	High	-		6	Noisy OB staves with updated DCol DB	Ivan, Artem, Jory	Urgent -	31/5/2024	Closed •	l	oniaiatoa pattorn			
19	WinCC	Review of the alerts in WinCC	Sergey Markus Artem Jory Felix Jian	High			7	EOR gets stuck when the Kafka information isn't av	Markus, Artem, Jory	High 💌	31/5/2024	Closed -	L IT	Threshold scan: Not-OK lanes in			
20	WinCC		Sergey	High	-					•				everal (~5) IB staves (4	ssigned to:		
21	WinCC	Transition states during Safe/R SUITINGLY LOUIE	Sergey	High	-					•		•	۰ ۸	rtem Iony Ivan Priorit	/: High		
22	WinCC	Fail PER without failing the environment creation	Sergey	High	-									tatus: In progress) [On	v. riigii, pration:ID:11		
23	WinCC	Clear indication of stayes that failed SOB or EOP	Markus Sergey	High	-	Open 16								iaius. In progress) [Op	station.iD. Ij		
24	WinCC	Safa machanisme	Sorgov Markup	High									N	otes: - 20240603 [.]			
24	WINCC	Vinico Sergey, Markus Tingri (1911)						Dotail	od tobl	d tabla				 retuning detector first => debug the 			
			20		Detail	eu labi					problematic staves						
	T = Summary · Operation · Its comm · Wince · RU · PU · CAEN · Calibration · QC · Reconstruct													 20240614 Succesfuly retuned I 	3 to 100e- and 120		
						22								e-			
						24								 No no unsuccess in Some chins still could 	THR scan		
														primarily from L2 12	L2 14	4 F	
+ = Summary - Operation - ITSComm -											CAEN -	Calibration	•		-	<	



- No major interventions are planned for now
- Investigating a few IB problematic chips during data taking and threshold scans
- Major development and maintenance activities include:
 - Optimizing in-run recovery procedures and tools
 - Stability of PFR/SOR/EOR
 - Improvement on detector safe mechanisms
 - Restoring lost WinCC panels
 - A short dry-run before data taking to check data path integrity
- TS2 scheduled from Oct. 21st to 23rd before pp ref and Pb-Pb runs
- YETS starts on Nov. 25th:
 - Reassessing the cooling leak on L5_19 (OB53)



Summary



- Several interventions on PU/RU cables and the CAEN power module
- Significant developments and progress in DCS
- Successful tests and improvements during TS1
- Many further developments are needed to enhance data taking stability/efficiency and to better protect the detector