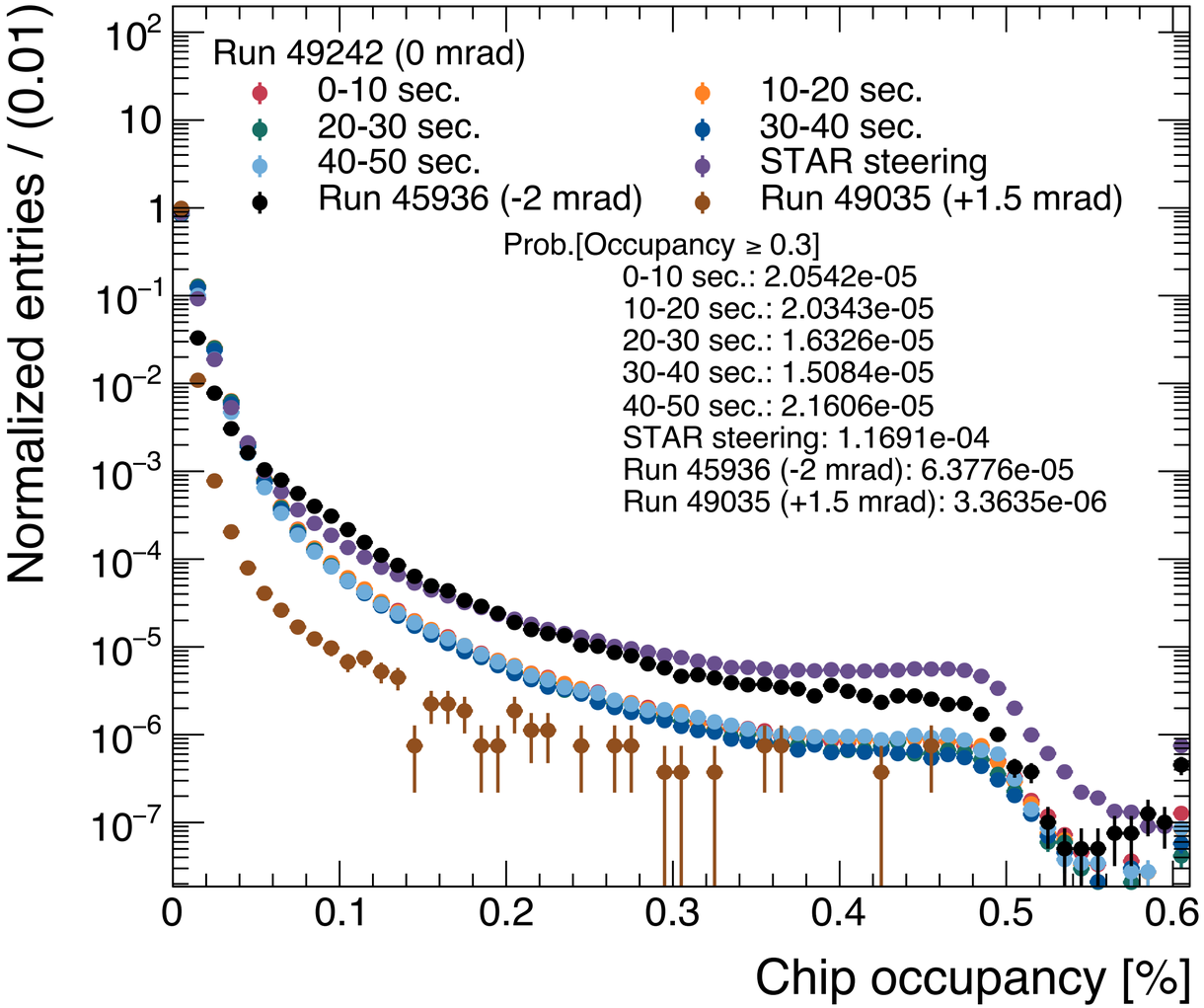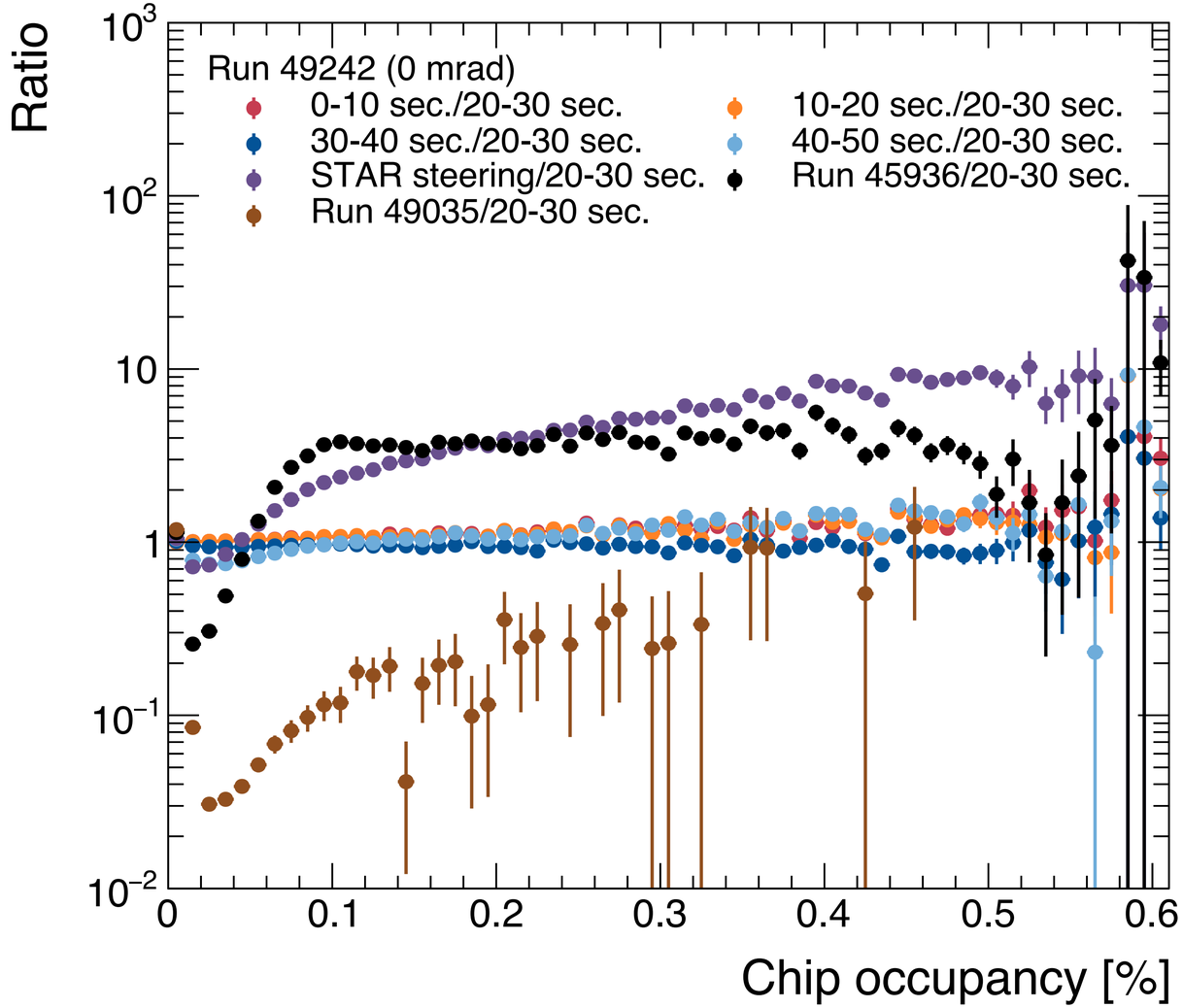Chip occupancy