

FST t-board meeting

A meeting was called to discuss the status of T-boards and the soldering of the inner cables to the boards.

A mistake was found in the description tables for how the soldering was to be done, and the production was put on hold.

Present: Gerard, Zhenuy, Mike, Jianing, Flemming.

Agenda and background material were posted on

<https://drupal.star.bnl.gov/STAR/event/2019/10/18/star-forward-silicon-tracker-meeting>

- a) There was mis communication so the soldering instruction for V2.1 (relative to 2.0) was wrong.
- b) The t-board version 2.0 test cable is properly cabled and can be used.
- c) The spreadsheet was made from the schematic, and then we (Mike&Steve) used the spreadsheet to assign wire pairs to pins on the D-Sub matching signals on the T-Board. The picture was then created from the schematic (or spreadsheet). The picture Mike circulated during the meeting was made by Steve, and it applied only to the V2.0 T-Board

Initially, Mike was using an outdated copy of the spreadsheet which was correct for V1.0 (Not produced). The picture from Steve was for V2.0, and the cable drawing (The autocad drawing on STAR titleblock) sent to the vendor was originally for V1.0, up-rev'd to "Revision B" to match the V2.0 T-Board, but the spreadsheet Mike was given was not updated to match. The "Via Ref Des" value, the "Wxx" number in the picture, changed again with the V2.1 T-Board, which is where the confusion began for me, this is when Mike wisely stopped production on the assembly, and we held the meeting.

- d) It was agreed that t-board V2.1 and cables just require an updated table for soldering that should be provided to assembly house. It was agreed that documentation should contain all needed info and should NOT have any obsolete info (like historical stuff). The proper documentation should be kept on official site. Mike will update information and circulate to this group, where Gerard and Jianing will review and sign-off.
- e) For future procurement needs to ensure complete documentation on proper site (not e-mails) and signoffs before production.

After the meeting Mike updated the documentation on a blog

I've created a new picture of the PCB with the Via Ref Des displayed, created a corrected cable drawing, created a stand-alone Excel spreadsheet for the V2.1 T-Board, and written up a statement of work. I've attached it, and put it as a blog post here: <https://drupal.star.bnl.gov/STAR/blog/capotosto/fst-v21-t-board-prototype-assembly-documentation-10182019>