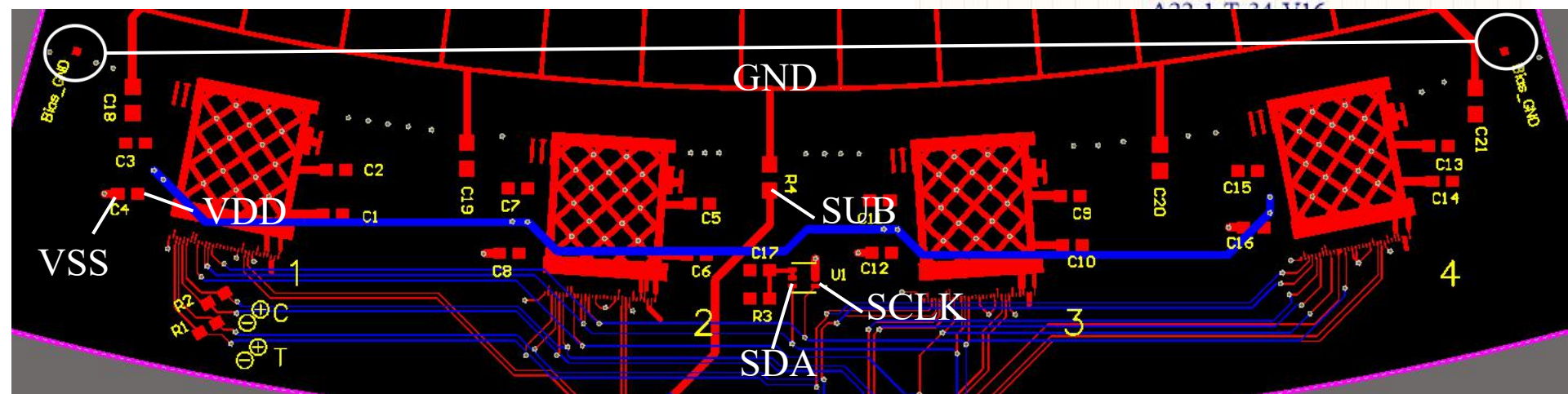


# Open connection test: inner hybrid

- ❑ **VSS:** pin 1# & C4 (C8, C12, C16) -2#
- ❑ **VDD:** pin 32# & C4 (C8, C12, C16) -1#
- ❑ **GND:** pin 7# & Bias\_GND
- ❑ **SUB:** pin 33# & R4 - 2#
- ❑ **CLK\_P:** pin 17# & R2-1#
- ❑ **CLK\_N:** pin 15# & R2-2#
- ❑ **TRG\_P:** pin 23# & R1-1#
- ❑ **TRG\_N:** pin 21# & R1-2#
- ❑ **SCLK:** pin 27# & U1-1#
- ❑ **SDA:** pin 29# & U1-6#

		JA1	
VSSa	1	2	OUT3 P
VSSa	3	4	OUT3 N
VSSa	5	6	OUT2 P
GND	7	8	OUT2 N
GND	9	10	OUT1 P
GND	11	12	OUT1 N
VSSa sense	13	14	OUT0 P
CLKa N	15	16	OUT0 N
CLKa P	17	18	
VDDa sense	19	20	
TRGa N	21	22	
TRGa P	23	24	
GNDa sense	25	26	VDDa
SCLKa	27	28	VDDa
SDAa	29	30	VDDa
RSTa	31	32	VDDa
SUB0	33	34	



# Open connection test: outer hybrid

- ❑ **VSS:** pin 1# & C4 (C8, C12, C16) -2#
- ❑ **VDD:** pin 32# & C4 (C8, C12, C16) -1#
- ❑ **GND:** pin 7# & Bias\_GND
- ❑ **SUB:** pin 33# & R4 (R5) - 2#
- ❑ **CLK\_P:** pin 17# & R2-2#
- ❑ **CLK\_N:** pin 15# & R2-1#
- ❑ **TRG\_P:** pin 23# & R1-2#
- ❑ **TRG\_N:** pin 21# & R1-1#
- ❑ **SCLK:** pin 27# & U1-1#
- ❑ **SDA:** pin 29# & U1-6#

JA1			
VSSa	1	2	OUT3 P
VSSa	3	4	OUT3 N
VSSa	5	6	OUT2 P
GND	7	8	OUT2 N
GND	9	10	OUT1 P
GND	11	12	OUT1 N
VSSa sense	13	14	OUT0 P
CLKa N	15	16	OUT0 N
CLKa P	17	18	
VDDa sense	19	20	
TRGa N	21	22	
TRGa P	23	24	
GNDa sense	25	26	VDDa
SCLKa	27	28	VDDa
SDAa	29	30	VDDa
RSTa	31	32	VDDa
SUB0	33	34	

A22-1-T-34-V16

