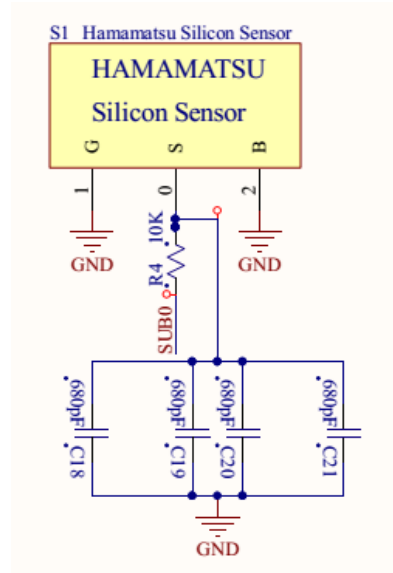
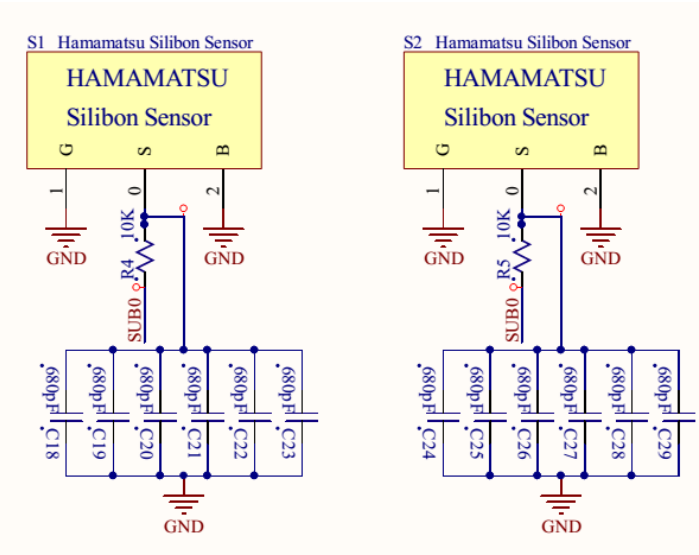


Summary of FST detector bias connections

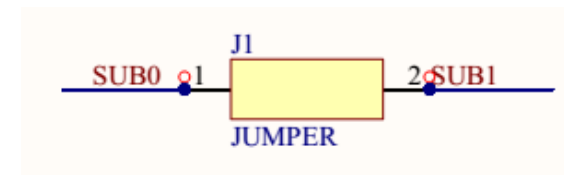
G. Visser 8/11/20

Outer hybrid (2.3, current?)

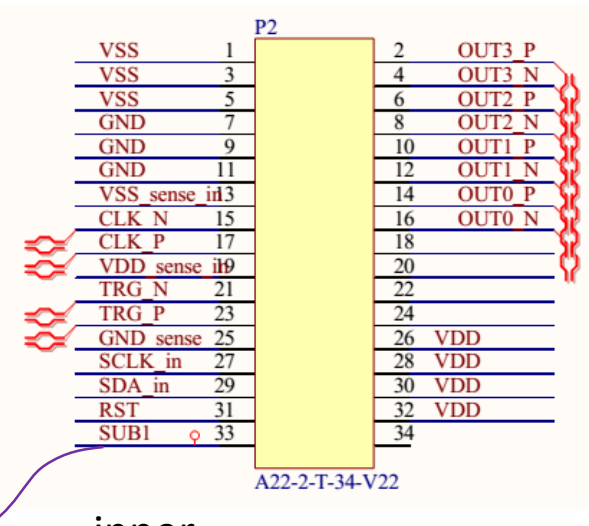
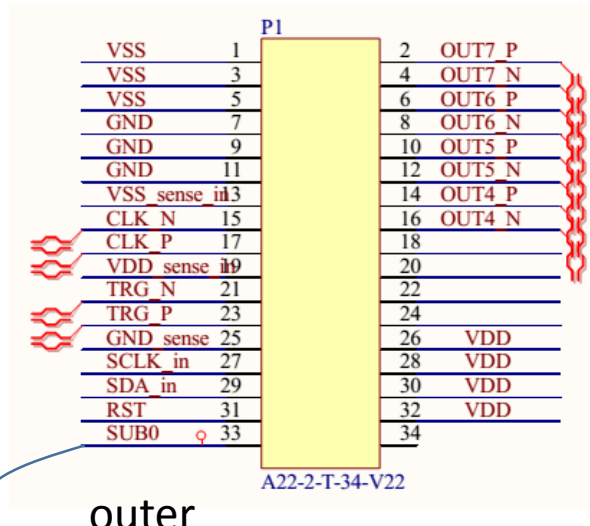
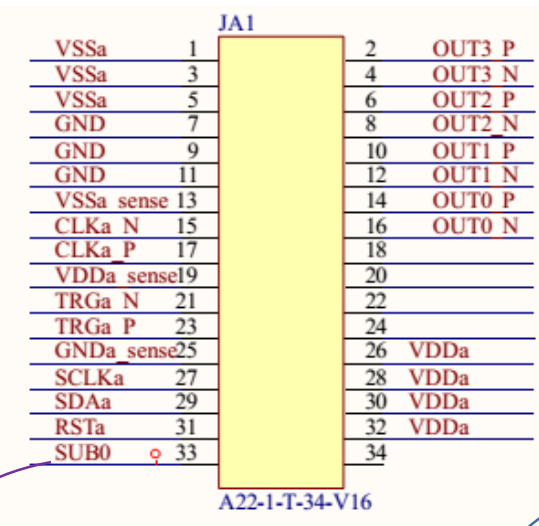
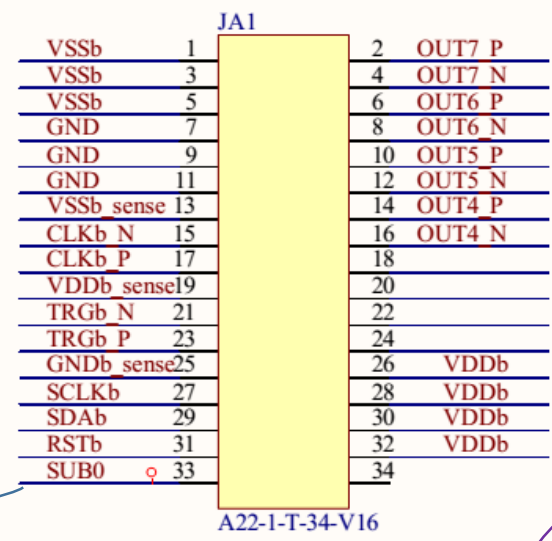
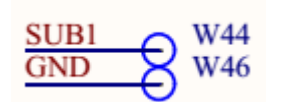
Inner hybrid (2.3, current?)



T-board (3.0, current)



we can/should omit this entirely



outer

inner

to T-board

----- purple cable -----

to PPB

P1

inner bias

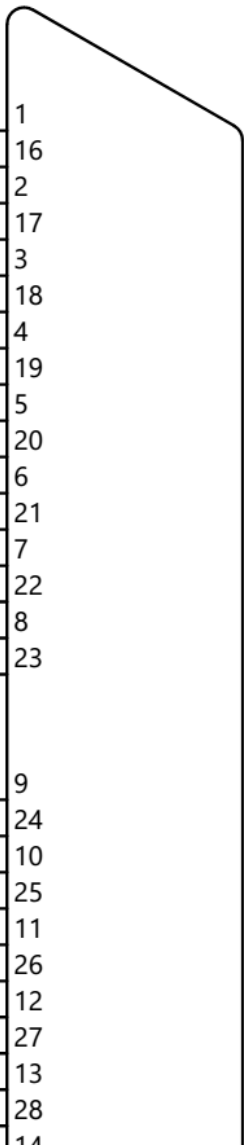
inner bias ret.

CONNECT: W44 SUB1 +	BLACK		1
CONNECT: W46 SUB1 -	RED		16
CONNECT: W18 OUT7 +	BLACK		2
CONNECT: W19 OUT7 -	WHITE		17
CONNECT: W14 OUT6 +	BLACK		3
CONNECT: W15 OUT6 -	GREEN		18
CONNECT: W10 OUT5 +	BLACK		4
CONNECT: W11 OUT5 -	BLUE		19
CONNECT: W7 OUT4 +	BLACK		5
CONNECT: W9 OUT4 -	BROWN		20
CONNECT: W61 OUT3 +	BLACK		6
CONNECT: W62 OUT3 -	YELLOW		21
CONNECT: W58 OUT2 +	BLACK		7
CONNECT: W57 OUT2 -	ORANGE		22
CONNECT: W54 OUT1 +	RED		8
CONNECT: W53 OUT1 -	GREEN		23

CONNECT: W52 OUT0 +	RED		9
CONNECT: W50 OUT0 -	WHITE		24
CONNECT: W20 TRG +	RED		10
CONNECT: W22 TRG -	BLUE		25
CONNECT: W24 CLK +	RED		11
CONNECT: W26 CLK -	YELLOW		26
CONNECT: W3 SUB0 +	RED		12
CONNECT: W1 SUB0 -	BROWN		27
CONNECT: W16 VSS_SENSE	RED		13
CONNECT: W17 GND_SENSE	ORANGE		28

outer bias

outer bias ret.



D-SUB CONNE
173113-0101

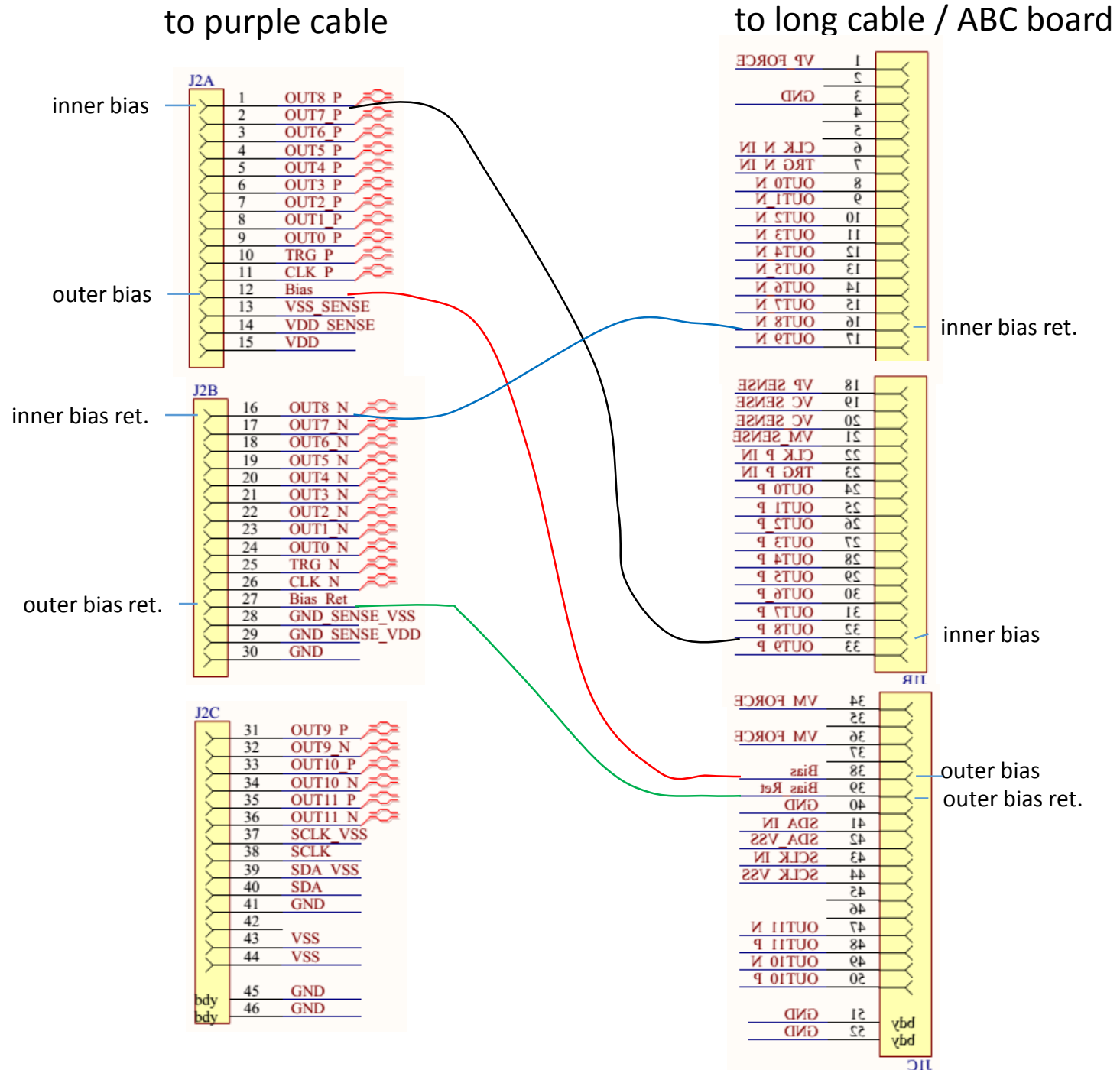
D-SUB CONTA

D-SUB BACKSH
MPN: 8655MH

IST Patchpanel Board

UNMODIFIED

i.e. we have exactly un-do what was done before. which is a bit of a shame that we did that to all boards without a full plan in hand.



long cable has straight pinout D50 – D50

ABC board (one half shown, other similar)
TO BE MODIFIED AS SHOWN

CUT TO ISOLATE NETS SIG8+/-

