- Rewrite the FPGA firmware
  - iTPC 100% finished. In debugging stages.
  - TPX 80% finished. Currently working on the main FPGA but it is very slow due to old tools.
  - Expect to be complete by Jan 2023. Debugging and verifying continues until March.
- Rewrite DAQ Framework for TPC
  - 90% finished. Working and in use. Monitoring tools and feature addition continues until Mar.
- Redo and evaluate cluster-finder
  - 95% finished. iTPC comparison complete. In debugging stages until March.
  - Requires TPX verification with tracking.
- Improve network connectivity
  - Network switches available. Need to recable ethernet networks.
- Add DAQ PCs and more EVBs
  - DAQ PCs will not be required. 8 EVB machines obtained from SDCC. 6 new EVBs on order. Reconfiguration in progress.