

Activity ID	Activity Name	Dur	% Complete	BL Start	BL Finish	WS Start	WS Finish	Exp Finish	Float	FY2019	FY2020	FY2021
Total		365	71.41%	10-01-18	09-30-19	09-30-18 A	04-17-20	04-17-20	1694	04-17-20, Total		
6.04 Status ATLAS Upgrade Phase II - LAr		365	71.41%	10-01-18	09-30-19	09-30-18 A	04-17-20	04-17-20	1694	04-17-20, 6.04 Status ATLAS Upgrade Phase II - LAr		
6.04 Status.M Phase 2 Milestones		0	0%	10-01-18	01-02-19	10-01-18 A	04-01-19 A			04-01-19 A, 6.04 Status.M Phase 2 Milestones		
6.04 Status.M.01 Funding Milestones		0	0%	10-01-18	01-02-19	10-01-18 A	04-01-19 A			04-01-19 A, 6.04 Status.M.01 Funding Milestones		
AP2 Task Group - Temp: 00 FY Funding		0	0%	10-01-18	01-02-19	10-01-18 A	04-01-19 A			04-01-19 A, AP2 Task Group - Temp: 00 FY Funding		
FY2019	FY2019 Funding	0	100%	10-01-18		10-01-18 A				FY2019 Funding		
FY2019Q2	FY Qrt 2 2019 Funding	0	100%	01-02-19		04-01-19 A				FY Qrt 2 2019 Funding		
6.04 Status.90 L2 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	09-30-19, 6.04 Status.90 L2 Project Management		
AP2 Task Group - Temp: L2 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	09-30-19, AP2 Task Group - Temp: L2 Project Management		
MGT41020	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Project Management - FY19		
6.04 Status.01 FE Electronics		293	59.88%	10-01-18	09-30-19	09-30-18 A	12-27-19	09-30-19	1772	12-27-19, 6.04 Status.01 FE Electronics		
6.04 Status.01.90 L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	09-30-19, 6.04 Status.01.90 L3 Project Management		
AP2 Task Group - Temp: L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	09-30-19, AP2 Task Group - Temp: L3 Project Management		
MGT43100	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Project Management - FY19		
6.04 Status.01.01 FE Electronics-Columbia		292	34.56%	10-01-18	05-28-19	09-30-18 A	12-27-19	07-12-19	1772	12-27-19, 6.04 Status.01.01 FE Electronics-Columbia		
6.04 Status.01.01.03 LAr FE ADC Pre-Prototype 2 Chip Cycle		0	0%	10-01-18	10-01-18	09-30-18 A	09-30-18 A			09-30-18 A, 6.04 Status.01.01.03 LAr FE ADC Pre-Prototype 2 Chip Cycle		
AP2 Task Group - Temp: 03 Pre-prototype 2 Chip Cycle		0	0%	10-01-18	10-01-18	09-30-18 A	09-30-18 A			09-30-18 A, AP2 Task Group - Temp: 03 Pre-prototype 2 Chip Cycle		
FEE410215	Milestone: Ship to UT Austin Packaged	0	100%		10-01-18		09-30-18 A			Milestone: Ship to UT Austin Packaged		
6.04 Status.01.01.04 LAr FE ADC Pre-Prototype 2 Chip Test		43	100%	11-28-18	01-30-19	11-28-18 A	03-31-19 A			03-31-19 A, 6.04 Status.01.01.04 LAr FE ADC Pre-Prototype 2 Chip Test		
AP2 Task Group - Temp: 04 Pre-prototype 2 Chip Test		43	100%	11-28-18	01-30-19	11-28-18 A	03-31-19 A			03-31-19 A, AP2 Task Group - Temp: 04 Pre-prototype 2 Chip Test		
FEE410250A	AVAIL: Pre-Prototype 2 ADC	0	100%		11-28-18		11-28-18 A			AVAIL: Pre-Prototype 2 ADC		
FEE410252	Pre-Prototype 2 ADC Performance Evaluation	43	100%	11-28-18	01-30-19	11-28-18 A	03-31-19 A			Pre-Prototype 2 ADC Performance Evaluation		
FEE410260EX	External Milestone: ATLAS Specification Review	0	100%		01-30-19		01-30-19 A			External Milestone: ATLAS Specification Review		
6.04 Status.01.01.05 LAr FE ADC Pre-Prototype 3 Chip Cycle		252	43.47%	11-28-18	05-28-19	11-28-18 A	12-27-19	07-12-19	-107	12-27-19, 6.04 Status.01.01.05 LAr FE ADC Pre-Prototype 3 Chip Cycle		
AP2 Task Group - Temp: 05 Pre-prototype 3 Chip Cycle		252	43.47%	11-28-18	05-28-19	11-28-18 A	12-27-19	07-12-19	-107	12-27-19, AP2 Task Group - Temp: 05 Pre-prototype 3 Chip Cycle		
FEE410270	Pre-Prototype 3 ADC Schematics Design	43	90%	11-28-18	01-30-19	11-28-18 A	08-29-19	05-30-19	-107	Pre-Prototype 3 ADC Schematics Design		
FEE410270M	Material for Pre-Prototype 3 ADC Design	41	90%	11-28-18	01-28-19	11-28-18 A	08-27-19	05-30-19	-105	Material for Pre-Prototype 3 ADC Design		
FEE410280T	Travel for Pre-Prototype 3 ADC Physical Layout Design	42	30%	01-30-19	04-01-19	03-15-19 A	10-30-19	06-28-19	-107	Travel for Pre-Prototype 3 ADC Physical Layout Design		
FEE410280	Pre-Prototype 3 ADC Physical Layout Design	42	30%	01-30-19	04-01-19	03-15-19 A	10-30-19	06-28-19	-107	Pre-Prototype 3 ADC Physical Layout Design		
FEE410290T	Travel for Pre-Prototype 3 ADC Post Layout Simulation	40	5%	04-01-19	05-28-19	04-01-19 A	12-27-19	07-12-19	-107	Travel for Pre-Prototype 3 ADC Post Layout Simulation		
FEE410290	Pre-Prototype 3 ADC Post Layout Simulation	40	5%	04-01-19	05-28-19	04-01-19 A	12-27-19	07-12-19	-107	Pre-Prototype 3 ADC Post Layout Simulation		
6.04 Status.01.01.11 LAr FE ADC Other		20	100%	01-03-19	01-30-19	01-03-19 A	01-30-19 A			01-30-19 A, 6.04 Status.01.01.11 LAr FE ADC Other		
AP2 Task Group - Temp: Admin Adjustment		20	100%	01-03-19	01-30-19	01-03-19 A	01-30-19 A			01-30-19 A, AP2 Task Group - Temp: Admin Adjustment		
FEE410890M	Columbia-Austin Cost Regularization	20	100%	01-03-19	01-30-19	01-03-19 A	01-30-19 A			Columbia-Austin Cost Regularization		
6.04 Status.01.02 FE Electronics-UTAustin		292	65.35%	10-01-18	05-28-19	09-30-18 A	12-27-19	07-12-19	-107	12-27-19, 6.04 Status.01.02 FE Electronics-UTAustin		
6.04 Status.01.02.03 LAr FE ADC Pre-Prototype 2 Chip Irradiation Test		152	98.31%	10-01-18	05-08-19	09-30-18 A	08-29-19	05-30-19	-25	08-29-19, 6.04 Status.01.02.03 LAr FE ADC Pre-Prototype 2 Chip Irradiation Test		
No AP2 Task Group - Temp		152	98.31%	10-01-18	05-08-19	09-30-18 A	08-29-19	05-30-19	-25	08-29-19, No AP2 Task Group - Temp		
FEE420060R	REQD: From Nevis - Packaged ADC Pre-Prototype 2	0	100%	10-01-18		09-30-18 A				REQD: From Nevis - Packaged ADC Pre-Prototype 2		
FEE420040M	Material for Pre-Prototype 2 Irradiation Board FPGA Software	41	100%	10-26-18	12-26-18	10-26-18 A	02-15-19 A			Material for Pre-Prototype 2 Irradiation Board FPGA Software		
FEE420040	Pre-Prototype 2 Irradiation Board FPGA Software	42	100%	10-26-18	12-27-18	10-26-18 A	02-15-19 A			Pre-Prototype 2 Irradiation Board FPGA Software		
FEE420030M	Material for Pre-Prototype 2 Irradiation Board Fabrication	42	100%	10-26-18	12-27-18	11-01-18 A	12-27-18 A			Material for Pre-Prototype 2 Irradiation Board Fabrication		
FEE420030T	Travel for Pre-Prototype 2 Irradiation Board Fabrication	42	100%	10-26-18	12-27-18	11-01-18 A	12-27-18 A			Travel for Pre-Prototype 2 Irradiation Board Fabrication		
FEE420030	Pre-Prototype 2 Irradiation Board Fabrication	42	100%	10-26-18	12-27-18	11-01-18 A	12-27-18 A			Pre-Prototype 2 Irradiation Board Fabrication		
FEE420070	Pre-Prototype 2 ADC Irradiation Test	43	90%	03-11-19	05-08-19	01-30-19 A	08-29-19	05-30-19	-25	Pre-Prototype 2 ADC Irradiation Test		
FEE420070M	Material for Pre-Prototype 2 ADC Irradiation Test	42	90%	03-11-19	05-07-19	03-08-19 A	08-28-19	05-30-19	-24	Material for Pre-Prototype 2 ADC Irradiation Test		
FEE420070T	Travel for Pre-Prototype 2 ADC Irradiation Test	42	100%	03-11-19	05-07-19	03-08-19 A	04-30-19 A			Travel for Pre-Prototype 2 ADC Irradiation Test		
FEE420068R	REQD: From Hospital - Radiation Facility Access	0	100%	03-11-19		03-08-19 A				REQD: From Hospital - Radiation Facility Access		
6.04 Status.01.02.04 LAr FE ADC Pre-Prototype 3 Chip Cycle		252	38.85%	11-28-18	05-28-19	11-28-18 A	12-27-19	07-12-19	-107	12-27-19, 6.04 Status.01.02.04 LAr FE ADC Pre-Prototype 3 Chip Cycle		
No AP2 Task Group - Temp		252	38.85%	11-28-18	05-28-19	11-28-18 A	12-27-19	07-12-19	-107	12-27-19, No AP2 Task Group - Temp		
FEE4310254	Pre-Prototype 3 ADC Schematics Design	43	90%	11-28-18	01-30-19	11-28-18 A	08-29-19	05-30-19	-107	Pre-Prototype 3 ADC Schematics Design		
FEE4310254M	Material for Pre-Prototype 3 ADC Schematics Design	43	90%	11-28-18	01-30-19	11-28-18 A	08-29-19	05-30-19	-107	Material for Pre-Prototype 3 ADC Schematics Design		
FEE4310264	Pre-Prototype 3 ADC Physical Layout Design	42	30%	01-30-19	04-01-19	03-15-19 A	10-30-19	06-28-19	-107	Pre-Prototype 3 ADC Physical Layout Design		
FEE4310274T	Travel for Prototype 3 ADC Physical Layout Design	39	30%	04-01-19	05-24-19	03-15-19 A	12-26-19	07-12-19	-106	Travel for Prototype 3 ADC Physical Layout Design		
FEE4310274	Pre-Prototype 3 ADC Post Layout Simulation	40	5%	04-01-19	05-28-19	04-01-19 A	12-27-19	07-12-19	-107	Pre-Prototype 3 ADC Post Layout Simulation		
6.04 Status.01.03 FE Electronics-SMU		213	67.03%	10-02-18	06-14-19	10-02-18 A	08-29-19	06-14-19	37	08-29-19, 6.04 Status.01.03 FE Electronics-SMU		
6.04 Status.01.03.01 FE Electronics-SMU IpGBT Prototype		154	81.81%	12-10-18	05-29-19	12-10-18 A	08-16-19		45	08-16-19, 6.04 Status.01.03.01 FE Electronics-SMU IpGBT Prototype		
No AP2 Task Group - Temp		154	81.81%	12-10-18	05-29-19	12-10-18 A	08-16-19		45	08-16-19, No AP2 Task Group - Temp		
FEE30110	Evaluation Prototype IpGBT at CERN, Functional Tests	42	100%	12-10-18	02-07-19	12-10-18 A	02-07-19 A			Evaluation Prototype IpGBT at CERN, Functional Tests		
FEE30110T	Travel for Evaluation Prototype IpGBT at CERN, Functional Tests	41	100%	12-10-18	02-06-19	12-10-18 A	02-06-19 A			Travel for Evaluation Prototype IpGBT at CERN, Functional Tests		
FEE30130	Evaluation Prototype IpGBT at SMU, Detailed Functional Tests	44	90%	02-08-19	04-11-19	02-08-19 A	07-01-19		45	Evaluation Prototype IpGBT at SMU, Detailed Functional Tests		
FEE30140	Evaluation Prototype IpGBT at SMU, Detailed Parameter Measurements: Clock Jitter	33	20%	04-12-19	05-29-19	04-12-19 A	08-16-19		45	Evaluation Prototype IpGBT at SMU, Detailed Parameter Measurements: Clock Jitter		
6.04 Status.01.03.02 FE Electronics-SMU IpGBT Engineering Chip		121	27.5%	02-08-19	06-11-19	02-08-19 A	08-29-19	06-14-19	-36	08-29-19, 6.04 Status.01.03.02 FE Electronics-SMU IpGBT Engineering Chip		
No AP2 Task Group - Temp		121	27.5%	02-08-19	06-11-19	02-08-19 A	08-29-19	06-14-19	-36	08-29-19, No AP2 Task Group - Temp		
FEE30170	IpGBT Engineering Chip Design Work, Reproduce Findings in Tests via Simulation	43	50%	02-08-19	04-10-19	02-08-19 A	06-28-19		-36	IpGBT Engineering Chip Design Work, Reproduce Findings in Tests via Simulation		
FEE30180	IpGBT Engineering Chip Design Work, Implement Necessary Changes	43	5%	04-11-19	06-11-19	04-11-19 A	08-29-19	06-14-19	-36	IpGBT Engineering Chip Design Work, Implement Necessary Changes		
6.04 Status.01.03.05 FE Electronics-SMU FEB Datalink Pre-Prototype		148	100%	10-02-18	05-02-19	10-02-18 A	04-30-19 A			04-30-19 A, 6.04 Status.01.03.05 FE Electronics-SMU FEB Datalink Pre-Prototype		
No AP2 Task Group - Temp		148	100%	10-02-18	05-02-19	10-02-18 A	04-30-19 A			04-30-19 A, No AP2 Task Group - Temp		
FEE30612	Soft_IpGBT - Adapt the Code to KC705, Check with External Loop-back	20	100%	10-02-18	10-30-18	10-02-18 A	10-30-18 A			Soft_IpGBT - Adapt the Code to KC705, Check with External Loop-back		

Primary Baseline
 Remaining Work
 ATLAS Subsystem Critical
 Actual Work
 Critical Remaining Work
 Baseline Milestone

TASK filters: (Untitled Filter), Not Complete.

Activity ID	Activity Name	Dur	% Complete	BL Start	BL Finish	WS Start	WS Finish	Exp Finish	Float	FY2019	FY2020	FY2021
FEE30614	ADC_emulator - Code the Serial Output	22	100%	10-31-18	11-30-18	10-31-18 A	11-30-18 A					
FEE30616	ADC_emulator - add BCID Counter, Event Builder	42	100%	12-03-18	01-31-19	12-03-18 A	01-31-19 A					
FEE30618	Soft_lpGBT + ADC_Emulator Integration	20	100%	02-01-19	03-01-19	02-01-19 A	03-01-19 A					
FEE30620	Data Link Pre-Prototype Schematics Design w/ FPGA	44	100%	03-04-19	05-02-19	03-04-19 A	04-30-19 A					
6.04 Status.01.03.07 FE Electronics-SMU FEB Control-Link Pre-Prototype		124	56.61%	02-08-19	06-14-19	02-08-19 A	07-02-19		78			
No AP2 Task Group - Temp		124	56.61%	02-08-19	06-14-19	02-08-19 A	07-02-19		78			
FEE30760	Adapt LTDB Control Link Define Configuration	44	100%	02-08-19	04-11-19	02-08-19 A	04-11-19 A					
FEE30770	Control Link Pre-Prototype Schematics Design	45	10%	04-12-19	06-14-19	04-12-19 A	07-02-19		78			
6.04 Status.02 FEB2		278	76.61%	10-01-18	10-01-18 A	11-05-19	09-30-19		480			
6.04 Status.02.90 L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
AP2 Task Group - Temp: L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
MGT43180	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
6.04 Status.02.01 Columbia		268	76.61%	10-16-18	07-18-19	10-16-18 A	11-05-19		480			
6.04 Status.02.01.01 LAr FEB2 Analog Test PCB (PA/shaper + ADC)		268	76.3%	10-16-18	07-18-19	10-16-18 A	11-05-19		-51			
No AP2 Task Group - Temp		268	76.3%	10-16-18	07-18-19	10-16-18 A	11-05-19		-51			
FEB21015	Analog Test Board Schematics Design	30	100%	10-16-18	11-27-18	10-16-18 A	11-27-18 A					
FEB21020	Analog Test Board Layout Design	30	100%	11-28-18	01-10-19	11-28-18 A	01-10-19 A					
FEB21030T	Travel for Analog Test Board Design Review	22	100%	01-11-19	02-11-19	01-11-19 A	02-11-19 A					
FEB21030	Analog Test Board Design Review	23	100%	01-11-19	02-12-19	01-11-19 A	02-12-19 A					
FEB21042R	REQD: Received Pre-Prototype2 ADC Chips from Nevis/UT Austin	0	100%	01-30-19	01-30-19							
FEB21040	Analog Test Board Design Finalization	20	100%	02-13-19	03-13-19	02-13-19 A	03-13-19 A					
FEB21050M	Material for Analog Test Board Fabrication	43	70%	03-14-19	05-13-19	03-14-19 A	08-29-19		-49			
FEB21050	Analog Test Board Fabrication	45	70%	03-14-19	05-15-19	03-14-19 A	09-03-19		-51			
FEB21045R	REQD: Received Pre-Prototype2 PA/shaper Chips from BNL	0	100%	03-13-19	04-05-19							
FEB21060M	Material for Analog Test Board Functional Test	42	10%	05-16-19	07-16-19	04-27-19 A	11-01-19		-49			
FEB21060	Analog Test Board Functional Test	44	5%	05-16-19	07-18-19	04-27-19 A	11-05-19		-51			
6.04 Status.02.01.08 Software License		1	100%	03-01-19	03-01-19	03-01-19 A	03-01-19 A					
No AP2 Task Group - Temp		1	100%	03-01-19	03-01-19	03-01-19 A	03-01-19 A					
FEB21800M	Material for PADS Software License from Mentor FY19	1	100%	03-01-19	03-01-19	03-01-19 A	03-01-19 A					
6.04 Status.03 BE Electronics		365	67.35%	10-01-18	09-30-19	10-01-18 A	12-16-19		263			
6.04 Status.03.01 BE-Electronics-SB		206	67.54%	10-05-18	04-01-19	11-15-18 A	10-07-19		62			
6.04 Status.03.01.01 Hardware Evaluation Board		206	67.54%	10-05-18	04-01-19	11-15-18 A	10-07-19		62			
AP2 Task Group - Temp: Hardware Evaluation Board		206	67.54%	10-05-18	04-01-19	11-15-18 A	10-07-19		62			
BEE440080	RTM Evaluation Board Designs	45	100%	10-08-18	12-11-18	11-15-18 A	04-19-19 A					
BEE440085M	Material for Purchase Xilinx FPGA Evaluation Board and Develop Simple Example Firmware	1	100%	12-11-18	12-11-18	12-11-18 A	03-01-19 A					
BEE440090	RTM Evaluation Board Schematics, Infrastructure and Power	33	80%	12-12-18	01-29-19	01-01-19 A	07-19-19		41			
BEE440100	RTM Evaluation Board Firmware Development	43	10%	01-30-19	04-01-19	02-01-19 A	10-07-19		62			
BEE440085	Purchase Xilinx FPGA Evaluation Board and Develop Simple Example Firmware	45	80%	10-08-18	12-11-18	02-01-19 A	08-06-19		62			
BEE440095	RTM Evaluation Board Schematics, Electro-optics and FPGA	33	50%	01-30-19	03-18-19	02-01-19 A	07-19-19		41			
BEE440070	Milestone: I/O and Architecture Conceptual Design	0	100%	10-05-18	10-05-18	04-19-19 A						
BEE440075A	AVAIL: I/O and Architecture Conceptual Design	0	100%	10-05-18	10-05-18	04-19-19 A						
6.04 Status.03.02 BE Electronics-Arizona		266	65.67%	10-29-18	04-18-19	02-01-19 A	12-16-19		263			
6.04 Status.03.02.01 Firmware Evaluation Board		266	65.67%	10-29-18	04-18-19	02-01-19 A	12-16-19		263			
AP2 Task Group - Temp: Evaluation Board Firmware		266	65.67%	10-29-18	04-18-19	02-01-19 A	12-16-19		263			
BEE450080	RTM FW Base Block - Design FPGA Functionality Blocks	40	80%	02-21-19	04-18-19	02-01-19 A	12-16-19		263			
BEE450060	LASP FW TDAQ Block - Final Specifications	40	10%	10-29-18	12-24-18	04-01-19 A	08-21-19		263			
6.04 Status.03.90 L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
AP2 Task Group - Temp: L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
MGT43260	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19		0			
6.04 Status.04 System Integration		273	67.73%	10-01-18	09-30-19	10-01-18 A	04-17-20		-51			
6.04 Status.04.01 FEB2 Integration-BNL		241	59.95%	10-01-18	07-09-19	10-01-18 A	02-13-20		-10			
6.04 Status.04.01.01 LAr FEB2 Slice Test PCB Integration		241	59.95%	10-01-18	07-09-19	10-01-18 A	02-13-20		-10			
No AP2 Task Group - Temp		241	59.95%	10-01-18	07-09-19	10-01-18 A	02-13-20		-10			
SYS460010T	Travel for Slice Test Board Test Stand Electrical Design	43	100%	10-01-18	11-30-18	10-01-18 A	11-30-18 A					
SYS460010	Slice Test Board Test Stand Electrical Design	45	100%	10-01-18	12-04-18	10-01-18 A	03-01-19 A					
SYS460020	Slice Test Board Test Stand Mechanical Design	55	100%	12-05-18	02-22-19	12-03-18 A	03-01-19 A					
SYS460020T	Travel for Slice Test Board Test Stand Mechanical Design	52	100%	12-05-18	02-19-19	12-05-18 A	02-19-19 A					
SYS460030T	Travel for Slice Test Board Test Stand Fabrication	41	20%	02-25-19	04-22-19	02-25-19 A	06-26-19		98			
SYS460030	Slice Test Board Test Stand Fabrication	41	20%	02-25-19	04-22-19	02-25-19 A	11-29-19		-10			
SYS460030M	Material for Slice Test Board Test Stand Fabrication	1	5%	04-19-19	04-22-19	04-19-19 A	11-29-19		-10			
SYS460040T	Travel for Slice Test Board Test Stand Assembly	52	5%	04-23-19	07-05-19	04-23-19 A	02-13-20		-10			
SYS460040	Slice Test Board Test Stand Assembly	54	5%	04-23-19	07-09-19	04-23-19 A	01-31-20		-10			
6.04 Status.04.02 System Integration-BNL		268	80.44%	10-08-18	05-02-19	10-08-18 A	04-17-20		-51			
6.04 Status.04.02.01 LASP Technology Evaluation		3	0%	10-08-18	10-10-18	10-08-18 A	10-10-18 A					
No AP2 Task Group - Temp		3	0%	10-08-18	10-10-18	10-08-18 A	10-10-18 A					
SYS460495R	REQD: I/O and Architecture Conceptual Design	0	100%	10-08-18	10-08-18							
SYS460500	Milestone: LASP Tech Eval Board Evaluation Test	0	100%	10-10-18	10-10-18							
6.04 Status.04.02.02 LASP Prototype Integration		265	80.44%	10-12-18	05-02-19	10-12-18 A	04-17-20		-51			
No AP2 Task Group - Temp		265	80.44%	10-12-18	05-02-19	10-12-18 A	04-17-20		-51			

█ Primary Baseline
 █ Remaining Work
 █ ATLAS Subsystem Critical
█ Actual Work
 █ Critical Remaining Work
 ◆ Baseline Milestone

Activity ID	Activity Name	Dur	% Complete	BL Start	BL Finish	WS Start	WS Finish	Exp Finish	Float	FY2019	FY2020	FY2021
SYS460510	LASP Prototype Test Stand Electrical Design	45	100%	10-12-18	12-14-18	10-12-18 A	12-14-18 A			Gantt chart for Electrical Design		
SYS460520	LASP Prototype Test Stand Mechanical Design	55	100%	12-17-18	03-06-19	12-17-18 A	03-15-19 A			Gantt chart for Mechanical Design		
SYS460530	LASP Prototype Test Stand Fabrication	41	10%	03-07-19	05-02-19	03-07-19 A	04-17-20	04-17-20	-51	Gantt chart for Fabrication		
6.04 Status.04.90 L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for L3 Project Management		
AP2 Task Group - Temp: L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for AP2 Task Group - Temp: L3 Project Management		
MGT43340	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for Project Management - FY19		
6.04 Status.05 PA Shaper		253	91.28%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for PA Shaper		
6.04 Status.05.01 PA Shaper-BNL		173	91.28%	10-24-18	03-12-19	10-24-18 A	05-28-19		-28	Gantt chart for PA Shaper-BNL		
6.04 Status.05.01.03 LAr FE ASIC Pre-prototype2 Design		76	100%	10-24-18	02-11-19	10-24-18 A	03-25-19 A			Gantt chart for LAr FE ASIC Pre-prototype2 Design		
No AP2 Task Group - Temp		76	100%	10-24-18	02-11-19	10-24-18 A	03-25-19 A			Gantt chart for No AP2 Task Group - Temp		
ASIC460140	Pre-Prototype2 ASIC Submission	22	100%	10-24-18	11-23-18	10-24-18 A	12-06-18 A			Gantt chart for Pre-Prototype2 ASIC Submission		
ASIC460140M	Material for Pre-Prototype2 ASIC Submission	1	100%	11-26-18	11-26-18	11-15-18 A	11-15-18 A			Gantt chart for Material for Pre-Prototype2 ASIC Submission		
ASIC460150	Pre-Prototype2 ASIC Fabrication and Preparation for Testing and Preparation for Testing	43	100%	11-27-18	01-28-19	12-07-18 A	02-28-19 A			Gantt chart for Pre-Prototype2 ASIC Fabrication and Preparation for Testing		
ASIC460160M	Material for Pre-Prototype2 ASIC Packaging and Preparation for Testing	10	100%	01-29-19	02-11-19	01-29-19 A	03-25-19 A			Gantt chart for Material for Pre-Prototype2 ASIC Packaging and Preparation for Testing		
ASIC460160	Pre-Prototype2 ASIC Packaging and Preparation for Testing and Preparation for Testing	10	100%	01-29-19	02-11-19	01-29-19 A	03-25-19 A			Gantt chart for Pre-Prototype2 ASIC Packaging and Preparation for Testing		
6.04 Status.05.01.04 LAr FE ASIC Pre-prototype2 Test		145	75.11%	12-03-18	03-12-19	03-01-19 A	05-28-19		-28	Gantt chart for LAr FE ASIC Pre-prototype2 Test		
No AP2 Task Group - Temp		145	75.11%	12-03-18	03-12-19	03-01-19 A	05-28-19		-28	Gantt chart for No AP2 Task Group - Temp		
ASIC460200M	Material for Pre-Prototype2 Main Board Fabrication	44	100%	12-03-18	02-06-19	03-01-19 A	04-11-19 A			Gantt chart for Material for Pre-Prototype2 Main Board Fabrication		
ASIC460200	Pre-Prototype2 Main Board Fabrication	44	100%	12-03-18	02-05-19	03-01-19 A	04-11-19 A			Gantt chart for Pre-Prototype2 Main Board Fabrication		
ASIC460210M	Material for Pre-Prototype 2 ASIC Evaluation test	20	50%	02-12-19	03-12-19	03-29-19 A	05-28-19		-28	Gantt chart for Material for Pre-Prototype 2 ASIC Evaluation test		
ASIC460210	Pre-Prototype2 ASIC Evaluation Test	20	50%	02-12-19	03-12-19	03-29-19 A	05-28-19		-28	Gantt chart for Pre-Prototype2 ASIC Evaluation Test		
ASIC460200T	Travel for Pre-Prototype2 Main Board Fabrication	42	100%	12-03-18	02-01-19	04-01-19 A	04-11-19 A			Gantt chart for Travel for Pre-Prototype2 Main Board Fabrication		
6.04 Status.05.90 L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for L3 Project Management		
AP2 Task Group - Temp: L3 Project Management		253	0%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for AP2 Task Group - Temp: L3 Project Management		
MGT43420	Project Management - FY19	253	57.71%	10-01-18	09-30-19	10-01-18 A	09-30-19	09-30-19	0	Gantt chart for Project Management - FY19		

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