

Activity ID	Activity Name	Planned Duration	% Comp	BL Project Start	BL Project Finish	Start	Finish	Total Float	2019												2020												2021											
									Jan	F	M	Apr	M	Jun	Jul	Aug	S	Oct	N	D	Jan	F	M	Apr	M	Jun	Jul	Aug	S	Oct	N	D	Jan	F	M	Apr	M	Jun	Jul	A	S	Oct	N	D
6.04 Status LAr		597		25-Feb-19	03-Dec-20	25-Feb-19 A	01-Jul-21	1833	01-Jul-21, 6.04 Status LAr																																			
6.04 Status.01 FE Electronics		564		11-Apr-19	03-Dec-20	11-Apr-19 A	01-Jul-21	59	01-Jul-21, 6.04 Status.01 FE Electronics																																			
6.04 Status.01.01 FE Electronics-Columbia		114		11-Jul-19	20-Nov-19	02-Sep-19 A	28-Feb-20	226	28-Feb-20, 6.04 Status.01.01 FE Electronics-Columbia																																			
6.04 Status.01.01.05 LAr FE ADC Pre-Prototype 3 Chip Cycle		83		11-Jul-19	27-Sep-19	03-Sep-19 A	20-Dec-19 A		20-Dec-19 A, 6.04 Status.01.01.05 LAr FE ADC Pre-Prototype 3 Chip Cycle																																			
AP2 Task Group - Temp: 05 Pre-prototype 3 Chip Cycle		83		11-Jul-19	27-Sep-19	03-Sep-19 A	20-Dec-19 A		20-Dec-19 A, AP2 Task Group - Temp: 05 Pre-prototype 3 Chip Cycle																																			
FEE10310	Pre-Prototype 3 ADC Fabrication	81	100%	11-Jul-19	27-Aug-19	03-Sep-19 A	12-Dec-19 A		Pre-Prototype 3 ADC Fabrication																																			
FEE10320	Pre-Prototype 3 ADC Packaging	48	100%	27-Aug-19	27-Sep-19	01-Oct-19 A	20-Dec-19 A		Pre-Prototype 3 ADC Packaging																																			
FEE10320M	Material for Pre-Prototype 3 ADC Packaging	22	100%	27-Aug-19	27-Sep-19	02-Dec-19 A	20-Dec-19 A		Material for Pre-Prototype 3 ADC Packaging																																			
FEE10330	Milestone: Pre-prototype 3 ADC packaging complete	0	100%		27-Sep-19		20-Dec-19 A		Milestone: Pre-prototype 3 ADC packaging complete																																			
6.04 Status.01.01.06 LAr FE ADC Pre-Prototype 3 Chip Test		114		06-Sep-19	20-Nov-19	02-Sep-19 A	28-Feb-20	226	28-Feb-20, 6.04 Status.01.01.06 LAr FE ADC Pre-Prototype 3 Chip Test																																			
AP2 Task Group - Temp: 06 Pre-prototype 3 Chip Test		114		06-Sep-19	20-Nov-19	02-Sep-19 A	28-Feb-20	226	28-Feb-20, AP2 Task Group - Temp: 06 Pre-prototype 3 Chip Test																																			
FEE10350	Pre-Prototype 3 Board Fabrication	82	100%	06-Sep-19	04-Oct-19	02-Sep-19 A	02-Dec-19 A		Pre-Prototype 3 Board Fabrication																																			
FEE10350M	Material for Pre-Prototype 3 Board Fabrication	52	100%	06-Sep-19	04-Oct-19	15-Oct-19 A	02-Dec-19 A		Material for Pre-Prototype 3 Board Fabrication																																			
FEE10355	Milestone: Pre-prototype 3 ADC test board fabrication complete	0	100%		04-Oct-19		02-Dec-19 A		Milestone: Pre-prototype 3 ADC test board fabrication complete																																			
FEE10360	Pre-Prototype 3 ADC Functional Test	32	5%	04-Oct-19	20-Nov-19	02-Dec-19 A	28-Feb-20	184	Pre-Prototype 3 ADC Functional Test																																			
FEE10360M	Material for Pre-Prototype 3 ADC Functional Test	32	5%	04-Oct-19	20-Nov-19	02-Dec-19 A	28-Feb-20	226	Material for Pre-Prototype 3 ADC Functional Test																																			
FEE10360T	Travel for Pre-Prototype 3 ADC Functional Test	32	5%	04-Oct-19	20-Nov-19	02-Dec-19 A	28-Feb-20	226	Travel for Pre-Prototype 3 ADC Functional Test																																			
6.04 Status.01.02 FE Electronics-UTAustin		167		11-Jul-19	05-Dec-19	01-Aug-19 A	30-Jan-20	211	30-Jan-20, 6.04 Status.01.02 FE Electronics-UTAustin																																			
6.04 Status.01.02.05 LAr FE ADC Pre-Prototype 3 Chip Irradiation Test		167		11-Jul-19	05-Dec-19	01-Aug-19 A	30-Jan-20	211	30-Jan-20, 6.04 Status.01.02.05 LAr FE ADC Pre-Prototype 3 Chip Irradiation Test																																			
No AP2 Task Group - Temp		167		11-Jul-19	05-Dec-19	01-Aug-19 A	30-Jan-20	211	30-Jan-20, No AP2 Task Group - Temp																																			
FEE20290	Pre-Prototype 3 Irradiation Board Schematics Design	125	100%	11-Jul-19	08-Aug-19	01-Aug-19 A	30-Dec-19 A		Pre-Prototype 3 Irradiation Board Schematics Design																																			
FEE20300	Pre-Prototype 3 Irradiation Board Layout Design	103	70%	08-Aug-19	04-Oct-19	03-Sep-19 A	30-Jan-20	191	Pre-Prototype 3 Irradiation Board Layout Design																																			
FEE20330	Pre-Prototype 3 Irradiation Test PC (C++) Software	100	50%	04-Oct-19	05-Dec-19	04-Oct-19 A	30-Jan-20	211	Pre-Prototype 3 Irradiation Test PC (C++) Software																																			
FEE20320	Pre-Prototype 3 Irradiation Board FPGA Software	42	20%	04-Oct-19	05-Dec-19	02-Dec-19 A	30-Jan-20	211	Pre-Prototype 3 Irradiation Board FPGA Software																																			
FEE20320M	Material for Pre-Prototype 3 Irradiation Board FPGA Software	41	20%	04-Oct-19	04-Dec-19	02-Dec-19 A	30-Jan-20	211	Material for Pre-Prototype 3 Irradiation Board FPGA Software																																			
FEE20300T	Travel for Pre-Prototype 3 Irradiation Board Layout Design	40	50%	08-Aug-19	04-Oct-19	02-Dec-19 A	30-Jan-20	191	Travel for Pre-Prototype 3 Irradiation Board Layout Design																																			
6.04 Status.01.03 FE Electronics-SMU		564		11-Apr-19	03-Dec-20	11-Apr-19 A	01-Jul-21	59	01-Jul-21, 6.04 Status.01.03 FE Electronics-SMU																																			
6.04 Status.01.03.02 FE Electronics-SMU IpGBT Engineering Chip		564		11-Apr-19	14-Aug-19	11-Apr-19 A	01-Jul-21	-114	01-Jul-21, 6.04 Status.01.03.02 FE Electronics-SMU IpGBT Engineering Chip																																			
No AP2 Task Group - Temp		564		11-Apr-19	14-Aug-19	11-Apr-19 A	01-Jul-21	-114	01-Jul-21, No AP2 Task Group - Temp																																			
FEE30180	IpGBT Engineering Chip Design Work, Implement Necessary Changes	564	95%	11-Apr-19	11-Jun-19	11-Apr-19 A	01-Jul-21	-114	IpGBT Engineering																																			
FEE30190	IpGBT Engineering Chip Design Work, Overall Integration and Prepare for Submission	521	70%	12-Jun-19	14-Aug-19	12-Jun-19 A	01-Jul-21	-114	IpGBT Engineering																																			
6.04 Status.01.03.05 FE Electronics-SMU FEB Datalink Pre-Prototype		59		07-Oct-19	10-Dec-19	07-Oct-19 A	30-Jan-20	246	30-Jan-20, 6.04 Status.01.03.05 FE Electronics-SMU FEB Datalink Pre-Prototype																																			
No AP2 Task Group - Temp		59		07-Oct-19	10-Dec-19	07-Oct-19 A	30-Jan-20	246	30-Jan-20, No AP2 Task Group - Temp																																			
FEE30720M	Material for Data Link Pre-Prototype Test Setup	59	50%	07-Oct-19	07-Oct-19	07-Oct-19 A	30-Jan-20	246	Material for Data Link Pre-Prototype Test Setup																																			
FEE30730	Data Link Pre-Prototype Functional Tests	58	70%	08-Oct-19	10-Dec-19	08-Oct-19 A	30-Jan-20	246	Data Link Pre-Prototype Functional Tests																																			

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BEE10120	sRTM V1 Software and Firmware Integration	63	10%	29-Aug-19	31-Oct-19	04-Nov-19 A	03-Feb-20	243	sRTM V1 Software and Firmware Integration																																			
BEE10130	sRTM V1 Layout, FPGA	42	10%	15-May-19	15-Jul-19	03-Dec-19 A	03-Feb-20	188	sRTM V1 Layout, FPGA																																			
6.04 Status.03.02 BE Electronics-Arizona		237		22-Mar-19	27-May-20	01-Jul-19 A	05-Jun-20	169	05-Jun-20, 6.04 Status.03.02 BE Electronics-Arizona																																			
6.04 Status.03.02.01 Firmware Evaluation Board		237		22-Mar-19	10-Sep-19	01-Jul-19 A	05-Jun-20	169	05-Jun-20, 6.04 Status.03.02.01 Firmware Evaluation Board																																			
AP2 Task Group - Temp: Evaluation Board Firmware		237		22-Mar-19	10-Sep-19	01-Jul-19 A	05-Jun-20	169	05-Jun-20, AP2 Task Group - Temp: Evaluation Board Firmware																																			
BEE12080	SRTM FW Base Block - Design FPGA Functionality Blocks	128	60%	22-Mar-19	18-Apr-19	01-Jul-19 A	02-Jan-20	136	SRTM FW Base Block - Design FPGA Functionality Blocks																																			
BEE12085	SRTM FW - SOC Block - Develop Preliminary FW	128	80%	19-Apr-19	14-Jun-19	01-Jul-19 A	02-Jan-20	136	SRTM FW - SOC Block - Develop Preliminary FW																																			
BEE12090	SRTM FW - GBT Block - Develop Preliminary FW	64	80%	17-Jun-19	15-Jul-19	15-Oct-19 A	15-Jan-20	136	SRTM FW - GBT Block - Develop Preliminary FW																																			
BEE12110	LASP FW - Packetizer Block - Develop Preliminary FW	101	20%	17-May-19	15-Jul-19	15-Nov-19 A	09-Apr-20	169	LASP FW - Packetizer Block - Develop Preliminary FW																																			
BEE12112	LASP FW - MON Block - Develop Preliminary FW	141	10%	16-Jul-19	10-Sep-19	15-Nov-19 A	05-Jun-20	169	LASP FW - MON Block - Develop Preliminary FW																																			
6.04 Status.03.02.02 Firmware and Integration Pre-Prototype V1		144		01-Apr-20	27-May-20	01-Nov-19 A	27-May-20	136	27-May-20, 6.04 Status.03.02.02 Firmware and Integration Pre-Pr																																			
AP2 Task Group - Temp: Pre-Prototype V1 Firmware		144		01-Apr-20	27-May-20	01-Nov-19 A	27-May-20	136	27-May-20, AP2 Task Group - Temp: Pre-Prototype V1 Firmware																																			
BEE12120	SRTM FW - MGT Block - Develop Preliminary FW	144	10%	01-Apr-20	27-May-20	01-Nov-19 A	27-May-20	136	SRTM FW - MGT Block - Develop Preliminary FW																																			
6.04 Status.04 System Integration		378		25-Feb-19	07-Oct-20	25-Feb-19 A	19-Aug-20	265	19-Aug-20, 6.04 Status.04 System Integration																																			
6.04 Status.04.01 FEB2 Integration-BNL		378		25-Feb-19	02-Jun-20	25-Feb-19 A	19-Aug-20	200	19-Aug-20, 6.04 Status.04.01 FEB2 Integration-BNL																																			
6.04 Status.04.01.01 LAr FEB2 Slice Test PCB Integration		378		25-Feb-19	02-Jun-20	25-Feb-19 A	19-Aug-20	200	19-Aug-20, 6.04 Status.04.01.01 LAr FEB2 Slice Test P																																			
No AP2 Task Group - Temp		378		25-Feb-19	02-Jun-20	25-Feb-19 A	19-Aug-20	200	19-Aug-20, No AP2 Task Group - Temp																																			
SYS460030	Slice Test Board Test Stand Fabrication	210	60%	17-Sep-19	29-Nov-19	25-Feb-19 A	24-Jan-20	256	Slice Test Board Test Stand Fabrication																																			
SYS460030T	Travel for Slice Test Board Test Stand Fabrication	210	30%	17-Sep-19	29-Nov-19	25-Feb-19 A	24-Jan-20	256	Travel for Slice Test Board Test Stand Fabrication																																			
SYS460030M	Material for Slice Test Board Test Stand Fabrication	171	30%	17-Sep-19	29-Nov-19	19-Apr-19 A	24-Jan-20	256	Material for Slice Test Board Test Stand Fabrication																																			
SYS460040	Slice Test Board Test Stand Assembly	281	5%	02-Dec-19	31-Jan-20	23-Apr-19 A	01-Jun-20	256	Slice Test Board Test Stand Assembly																																			
SYS460040T	Travel for Slice Test Board Test Stand Assembly	281	5%	02-Dec-19	31-Jan-20	23-Apr-19 A	01-Jun-20	256	Travel for Slice Test Board Test Stand Assembly																																			
SYS460040M	Material for Slice Test Board Test Stand Assembly	228	5%	02-Dec-19	31-Jan-20	09-Jul-19 A	01-Jun-20	256	Material for Slice Test Board Test Stand Assembly																																			
SYS460060	Slice Test Board Test Stand Hardware Integration	227	5%	03-Feb-20	30-Mar-20	10-Jul-19 A	01-Jun-20	200	Slice Test Board Test Stand Hardware Integration																																			
SYS460060T	Travel for Slice Test Board Test Stand Hardware Integration	227	5%	03-Feb-20	30-Mar-20	10-Jul-19 A	01-Jun-20	200	Travel for Slice Test Board Test Stand Hardware Integration																																			
SYS460060M	Material for Slice Test Board Test Stand Hardware Integration	170	5%	03-Feb-20	30-Mar-20	01-Oct-19 A	02-Jun-20	200	Material for Slice Test Board Test Stand Hardware Integration																																			
SYS460070	Slice Test Board Test Stand Software Integration	225	5%	31-Mar-20	02-Jun-20	01-Oct-19 A	19-Aug-20	200	Slice Test Board Test Stand Software Integration																																			
SYS460028	LAr FEB2 Analog Test Board Test	35	100%	17-Jul-19	16-Sep-19	01-Nov-19 A	27-Dec-19 A		LAr FEB2 Analog Test Board Test																																			
SYS460028T	Travel for LAr FEB2 Analog Test Board Test	35	100%	17-Jul-19	16-Sep-19	01-Nov-19 A	27-Dec-19 A		Travel for LAr FEB2 Analog Test Board Test																																			
SYS460022	LAr FEB2 Analog Test Board Test Stand Mechanical Design	15	100%	25-Mar-19	12-Apr-19	02-Dec-19 A	30-Dec-19 A		LAr FEB2 Analog Test Board Test Stand Mechanical Design																																			
SYS460022T	Travel for LAr FEB2 Analog Test Board Test Stand Mechanical Design	15	100%	25-Mar-19	12-Apr-19	02-Dec-19 A	30-Dec-19 A		Travel for LAr FEB2 Analog Test Board Test Stand Mechanical Design																																			
SYS460023	LAr FEB2 Analog Test Board Test Stand Fabrication	25	100%	15-Apr-19	17-May-19	02-Dec-19 A	30-Dec-19 A		LAr FEB2 Analog Test Board Test Stand Fabrication																																			
SYS460023M	Material for LAr FEB2 Analog Test Board Test Stand Fabrication	25	100%	15-Apr-19	17-May-19	02-Dec-19 A	30-Dec-19 A		Material for LAr FEB2 Analog Test Board Test Stand Fabrication																																			

█ Primary Baseline
 █ Remaining Work
 ◆ Milestone
 █ Actual Work
 █ Critical Remaining Work
 ▼ Summary

